UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

NOTICE OF ALLOWANCE AND FEE(S) DUE

88488

7590

08/03/2009

Intellectual Property Law Office of David Lathrop No. 827 39120 Argonaut Way Fremont, CA 94538 EXAMINER

MCMAHON, DANIEL F

ART UNIT PAPER NUMBER

2117

DATE MAILED: 08/03/2009

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/588,636	08/04/2006	Kazuyoshi Okawa	NAA243	9797

TITLE OF INVENTION: SEMICONDUCTOR DEVICE TEST APPARATUS AND METHOD

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	11/03/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

or Fax (571)-273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where ar in m

indicated unless correct maintenance fee notifica	ted below or directed ot	herwise in Block 1, by (a) specifying a new corre	espondence address; an	d/or (b) indicating a sep	arate "FEE ADDRESS" for
CURRENT CORRESPOND	DENCE ADDRESS (Note: Use B	block 1 for any change of address)	Fee par	e(s) Transmittal. This co pers. Each additional pa	ertificate cannot be used	or domestic mailings of the for any other accompanying ent or formal drawing, must
No. 827 39120 Argonau	operty Law Office t Way	e of David Lathrop	Sta	ereby certify that this F	sufficient postage for fir	smission g deposited with the United st class mail in an envelope above, or being facsimile date indicated below.
Fremont, CA 94	1538					(Depositor's name)
			_			(Signature)
,						(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR	A A	TTORNEY DOCKET NO.	CONFIRMATION NO.
10/588,636	08/04/2006		Kazuyoshi Okawa		NAA243	9797
APPLN, TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FI	EE TOTAL FEE(S) DUE	E DATE DUE
nonprovisional	NO NO	\$1510	\$300		\$1810	11/03/2009
EXAM	MINER	ART UNIT	CLASS-SUBCLASS	٦		
	N. DANIEL F	2117	714-710000	J		
	lence address or indication	on of "Fee Address" (37	2. For printing on the	patent front page, list		
CFR 1.363). Change of corresp Address form PTO/S	oondence address (or Cha B/122) attached.	ange of Correspondence	(1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.			
☐ "Fee Address" inc	dication (or "Fee Address 02 or more recent) attack	s" Indication form				
PLEASE NOTE: Un recordation as set for (A) NAME OF ASSI	tless an assignee is iden th in 37 CFR 3.11. Com GNEE	tified below, no assignee pletion of this form is NO	T a substitute for filing ar (B) RESIDENCE: (CIT	patent. If an assignee is assignment. Y and STATE OR COU	UNTRY)	document has been filed for our entity
	are submitted: No small entity discount # of Copies	permitted)	b. Payment of Fee(s): (Ple A check is enclosed. Payment by credit ca The Director is hereboverpayment, to Dep	rd. Form PTO-2038 is	attached.	shown above) eficiency, or credit any an extra copy of this form).
	ns SMALL ENTITY stat	us. See 37 CFR 1.27.			ENTITY status. See 37 C	
NOTE: The Issue Fee ar interest as shown by the	nd Publication Fee (if rec records of the United St	quired) will not be accepte ates Patent and Trademark	d from anyone other than c Office.	the applicant; a register	ed attorney or agent; or t	he assignee or other party in
Authorized Signature	>		Date			
Typed or printed name				Registration No.		
an application. Confider submitting the complete this form and/or suggest Box 1450, Alexandria, V Alexandria, Virginia 22.	ntiality is governed by 35 application form to the tions for reducing this but Virginia 22313-1450. Do 313-1450.	5 U.S.C. 122 and 37 CFR e USPTO. Time will vary urden, should be sent to th O NOT SEND FEES OR	1.14 This collection is es	stimated to take 12 min vidual case. Any comn ser, U.S. Patent and Tra O THIS ADDRESS. S.	utes to complete, includinents on the amount of tidemark Office, U.S. Dep END TO: Commissioner	d by the USPTO to process) ng gathering, preparing, and me you require to complete vartment of Commerce, P.O. for Patents, P.O. Box 1450,
Chaci are raperwork Ke	The of 1775, IIO	persons are required to re	spond to a concention of the	rormanon unicas it uisp	my s a varia Olvid collido	i namooi.

PTOL-85 (Rev. 08/07) Approved for use through 08/31/2010.

OMB 0651-0033

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE



United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/588,636	08/04/2006	Kazuyoshi Okawa	NAA243	9797	
88488 73	590 08/03/2009		EXAMINER		
Intellectual Prope	erty Law Office of Da	MCMAHON, DANIEL F			
No. 827	•		ART UNIT	PAPER NUMBER	
39120 Argonaut W Fremont, CA 9453			2117 DATE MAILED: 08/03/200	9	

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 246 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 246 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 (571)-272-4200.

	Application No.	Applicant(s)		
	10/588,636	OKAWA ET AL.		
Notice of Allowability	Examiner	Art Unit		
	DANIEL F. MCMAHON	2117		
The MAILING DATE of this communication apperall claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313 1. This communication is responsive to June 30, 2009.	(OR REMAINS) CLOSED in the or other appropriate communing GHTS. This application is sub-	nis application. If not included cation will be mailed in due course. 1		
2. ☑ The allowed claim(s) is/are <u>1-3 and 5-14</u> .				
 3. Acknowledgment is made of a claim for foreign priority una) a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	been received. been received in Application	No	the	
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be submit	ENT of this application. itted. Note the attached EXAM	INER'S AMENDMENT or NOTICE C		
INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient. 5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted. (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d). 6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.				
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Sum Paper No./M 7. ☐ Examiner's An	mal Patent Application nmary (PTO-413), ail Date nendment/Comment atement of Reasons for Allowance		

Application/Control Number: 10/588,636 Page 2

Art Unit: 2117

DETAILED ACTION

Reasons for Allowance

1. The following is an examiner's statement of reasons for allowance:

The present invention includes a semiconductor device test apparatus comprising a test processor which applies a test signal to a semiconductor memory device under test and a repair analysis computing unit which performs repair analysis.

The claimed invention recites features such as: "a repair analysis computing unit which performs repair analysis of the defective memory cell information to determine repair whether the defective memory cells are repairable; a general-purpose repair analysis part comprising: a memory repair analysis program; a user analysis program storage storing a user analysis program which is designated to perform, based on specific repair conditions, repair analysis of the defective memory cell information for a specific type memory device other than the regular type memory devices that has a specific redundancy structure and has the specific repair conditions; and an analysis control part with means for performing, when the specific type memory device is subjected to repair analysis, repair analysis for the specific type memory device in accordance with the user functions of the user analysis program which are inserted to the user function insertion points of the memory repair analysis program based on the specific repair conditions.

The prior arts of record (Oikawa et al. U.S. Patent 6,459,292 (herein Oikawa), in view of "GNU Emacs 19 Manual" by Richard M. Stallman (herein Stallman)) do not teach the same device.

Page 3

Oikawa teaches: a semiconductor device test apparatus (abstract): a test processor which applies a test signal to a semiconductor device under test and obtains information about a defective memory cell from a response signal (figure 1, element 20); and a repair analysis computing unit which performs repair analysis of the defective memory cell information to determine a way to repair the defective memory cell (figure 1, element 50 – 5F).

Stallman teaches: a user function means for inserting user functions of a user-specified user analysis program to user function insertion points between operations of the memory repair analysis program to make a change to an analysis program (section 30.2.3 Hooks, paragraph 1-3).

However, the prior arts of record fail to disclose, teach, or suggest in combination: a user analysis program storage storing a user analysis program which is designated to perform, based on specific repair conditions, repair analysis of the defective memory cell information for a specific type memory device other than the regular type memory devices that has a specific redundancy structure and has the specific repair conditions; and an analysis control part with means for performing, when the specific type memory device is subjected to repair analysis, repair analysis for the specific type memory device in accordance with the user functions of the user analysis

Application/Control Number: 10/588,636 Page 4

Art Unit: 2117

program which are inserted to the user function insertion points of the memory repair analysis program based on the specific repair conditions.

Claims 1 - 3, and 5 - 14 are allowable over the prior arts of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DANIEL F. MCMAHON whose telephone number is (571)270-3232. The examiner can normally be reached on M-Th 8am-5pm(EST).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kevin Ellis can be reached on (571) 272-4205. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Application/Control Number: 10/588,636 Page 5

Art Unit: 2117

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Dfm 07/30/09

/Kevin L Ellis/ Supervisory Patent Examiner, Art Unit 2117